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Date: March 15, 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant

Oliver Gehring et al.

Applic. No.

10/045,278

Filed

January 14, 2002

Title

Method for Fabricating Embedded Nonvolatile Semiconductor

Memory Cells

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks, Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States Patent No. 5,395,778 (Walker), dated March 7, 1995;

United States Patent No. 5,723,355 (Chang et al.), dated March 3, 1998;

United States Patent No. 6,074,915 (Chen et al.), dated June 13, 2000;

United States Patent No. 6,121,087 (Mann et al.), dated September 19, 2000;

British Patent Application GB 2 347 016 A (Lam et al.), dated August 23, 2000;

International Publication WO 97/36332 (Dormans et al.), dated October 2, 1997.

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If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,

For Applicants

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